Jackie – Unique Aspects of the design

Use only 3-4 registers

Chose an accumulator with stack-like implementation

Start instruction from 0100

Use mem addr 0300 as stack pointer

Use mem addr 0200 as stack

An – Discuss changes that you would like to make to your design

Add more registers – faster and easier – couldn’t make them work

Better implement the Memory

Add interrupts

Add special input/output devices

Make the processor a pipeline

Donai – Aspects we found most challenging and interesting

Challenging – how to implement instructions

Memory

Testing – long and somewhat complicated

Clock

Interesting – don’t do operations directly with register, use mem and reg together

Donai – Unique problems and adventures with Xilinx

I hate Xilinx

Can’t use too many IOB devices

For testing – doesn’t give what is wrong, just a general area

Bo – Performance Data from Processor